

Publication from Shailesh KadakiaMatrix Writers & Publishers:

Books:

1. Unique Physics of Light and Astronomy, August 30, 2016
2. True Physics of Light Beyond Relativity, Second Edition, April 01, 2011
3. True Physics of Light Beyond Relativity, First Edition, May 20, 2010

White papers:

1. Unique Concepts of Skylativity[®], December 25, 2015
2. Are light waves different from EM waves? March 2, 2010
3. Light is a wave: Revisiting outcome of light's particle nature experiments, March 2010.

You tube:

1. <https://www.youtube.com/watch?v=kX5V3-j5f9w>
Search: Expert math tutor, Escondido, CA (San Diego area)
2. <https://www.youtube.com/watch?v=mPazgTIuwPU>
Search: Skylativity[®]: Remedy for Gaps in old EMF theories

HARRIS RF Communications, Inc.:

1. Software quality metric for Soft Defined Radios, May 30, 2009
2. First responder service radio with wireless test access ports, September 04, 2008
3. Proposed solution for immense increase in gas price, August 08, 2008
4. Explore the challenges of recruiting new college graduates, July 23, 2008
5. Huge savings in cost from new hiring process, July 30, 2008

NxtWave Communications, Inc. (ATI Research, Inc.):

1. Design for test and manufacturing of circuits for DVB signal reception, ITC 2001, Washington D.C.
2. Guide lines for writing large C++ programs and useful features of C++, November, 2001
3. Memory Built-in self test (MEMBIST) for memory fault isolation, October 05, 2000
4. Automatic test pattern generation (ATPG) using TetraMax, August 31, 2000
5. BIST for memories and scan using Logicvision tools, February 02, 2000
6. Built-in self test on Forward Error Correction logic, October 26, 1999

Crystal Semiconductors (Cirrus Logic), Motorola, IMS & Silicon Graphics:

1. Characterization of setup and hold times of latches for pathmill, December 21, 1998
2. A cache architecture with interleaved block and set bit-lines, December 19, 1997
3. A systematic approach to audit-promote; program APRLITE, October 06, 1995
4. An optimized design methodology for the development of mixed signal circuits, October 20, 1993

Texas Instruments Inc.:

1. Statistical procedure for development of MOS models, TI Tech. Report 06-91-05, June 10, 1991
2. A 4 Mega-bit EPROM with Laser blown fuse redundancy, TI Tech Report 03-90-05, June 12, 1990
3. Strategic project development of EPROM memories in today's market place, June 28, 1989
4. A 25-ns CMOS EPLD with input transition detection capability for powering down, TI Technical Journal, March-April 1989
5. TI's erasable PLDs are fast but don't need much power, Electronics, May 12, 1988
6. A EPROM memory with on chip ECC for yield enhancement, TI Tech Report 03-87-06, June 12, '87
7. A 1 Mb CMOS EPROM with 13.5 sq. micron cell, IEEE ISSCC digest February, '87, VII:WPM 7.2.

Patents Issued :**Year**

- | | | |
|--|------|-----------------------|
| 1. Bias circuitry for non-volatile memory array | 1992 | Serial No.: 5,132,933 |
| 2. Two transistor programmable memory cell...gate | 1992 | Serial No.: 5,078,498 |
| 3. High speed CMOS transition detector circuit | 1990 | Serial No.: 4,963,765 |
| 4. Integrated-circuit power-up pulse generator circuit | 1989 | Serial No.: 4,888,498 |
| 5. Single-ended sense amplifier...feedback | 1989 | Serial No.: 4,823,031 |

Provisional Patents & Ideas:**Year**

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|--|------|
| 1. An Aircraft Tracking System utilizing Radar Technology in Space | 2014 |
| 2. A Quick and Fully Automated Check-in System for Motels | 2013 |
| 3. First responder service Radio with wireless test port access | 2009 |
| 4. Advanced waveform for BLOS communication in WiFi Radio network | 2008 |
| 5. Silent mode radio system with iPod outlook | 2008 |
| 6. An electronic menu system for ordering entre at restaurants | 2008 |
| 7. Algorithm to secure data transfer on internet | 2008 |
| 8. A System for enhancing national security, Provisional No.60/668,528 | 2005 |
| 9. A Ski with cellular phone and GPS for life saving | 2005 |
| 10. Combat ID system for detecting friendly entity | 2005 |
| 11. Technique to uniquely identify diamonds | 2005 |
| 12. Static-RAM with reseted bits selected by encoded inputs | 2000 |
| 13. Byte/word error correction schemer using BIST | 1999 |